

Bias Voltage Circuit with Ultra Low Output Impedance

Damian Costa et al. Docket #1702

Express Mail # ER309411871US

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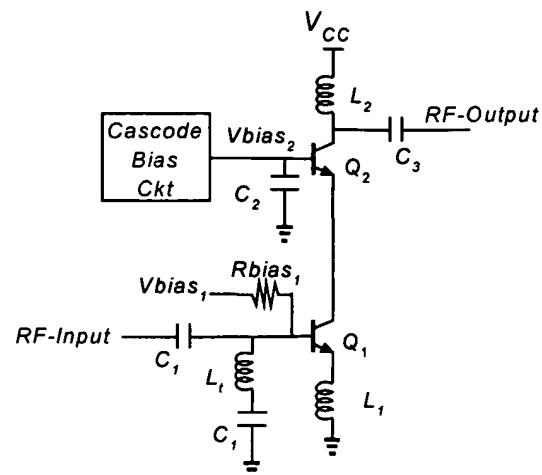


Figure 1

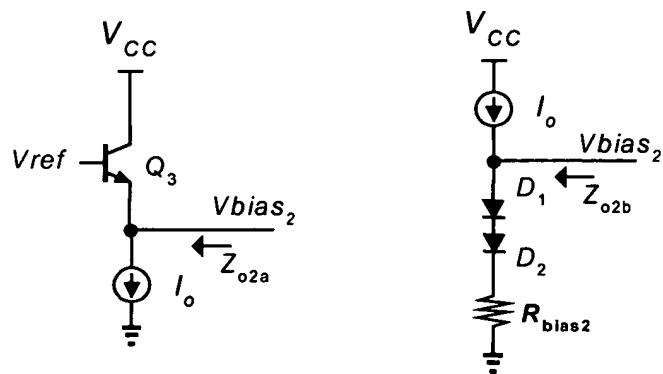


Figure 2(a)

Figure 2(b)

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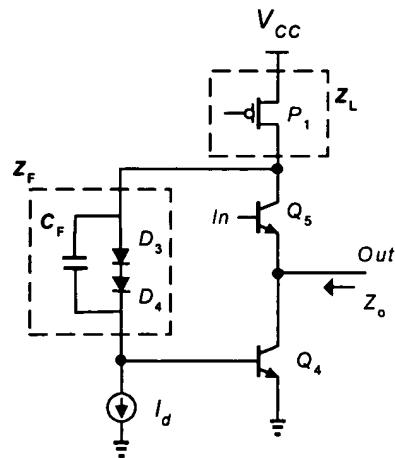


Figure 3

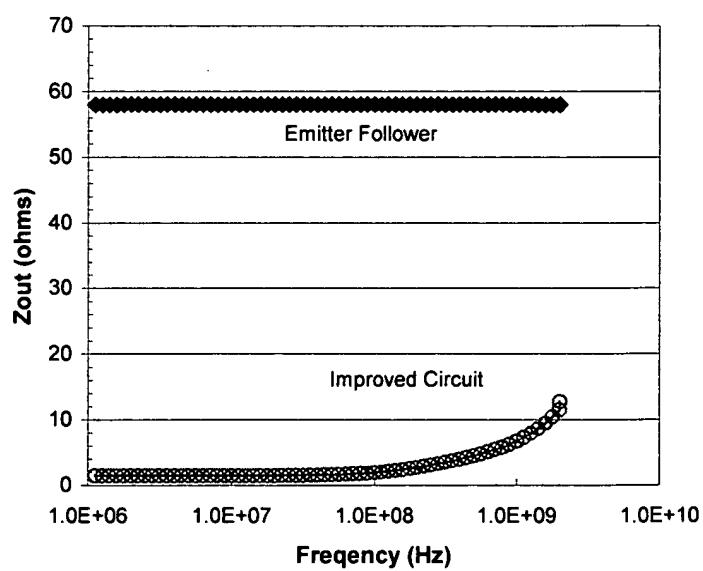


Figure 4